

U.S. Department of Commerce, Patent and Trademark Office	Atty Docket No.	Serial No. <u>08A 215</u>
	M-15339 US	Not yet assigned
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s)
(Use several sheets if necessary)		Xiang Zhu; Ming Qu; Yongmin Ge
Filing Date		Group <u>2816</u>
Herewith <u>4/2/04</u>		Not yet assigned

U.S. Patent Documents							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<u>GS</u>	AA	6,498,538	12/24/2002	Qu et al.	331	25	
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
<u>GS</u>	AQ	ISSCC 2003/Session 24/Clock Generation/Paper 24.1, 2003 IEEE International Solid-State Circuits Conference
<u>GS</u>	AR	"Differential CMOS Circuits for 622-MHz/933-MHz Clock And Data Recovery Applications," by Hormoz Djahanshahi et al., IEEE Journal of Solid-State Circuits, Vol. 35, No. 6, June 2000
	AS	

Examiner <u>[Signature]</u>	Date Considered <u>8/12/05</u>
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.</p>	